# **PAC 40 SERIES RELAYS**



TECHNICAL SPECIFICATIONS						
TY	PE	PAC				
TERMINAL TYPE		Solder / Lugs				
CONTACT CONFIGURATION		1 N/O				
RATED CARRYING CURRENT (RESISTIVE) AT 14 VDC		40A				
CONTACT MATERIAL		Silver alloy				
INITIAL CONTACT RESISTANCE (MAX)		0.050 Ω				
COIL NOMINAL	DC	12 - 24 V				
VOLTAGES	AC	-				
OPERATING POWER MIN-MAX)FOR DC COIL		1.6 W				
DIELECTRIC STRENGTH BETWEEN	OPEN CONTACT	500 <b>V</b> <sub>RMS</sub>				
	COIL TO CONTACT	750 V <sub>RMS</sub>				
INSULATION RESISTANCE AT 500 VDC AT 27°C & 65% RH		100 ΜΩ				
OPERATE TIME (MAX)		9 ms				
RELEASE TIME (MAX)		5 ms				
AMBIENT TEMPERATURE		-40°C To + 85°C				
ELECTRICAL LIFE (NO OF OPERATIONS)		10 <sup>5</sup>				
MECHANICAL LIFE (NO OF OPERATIONS)		10 <sup>6</sup>				
ALL DIMENSIOI (W X L X H		26.3 x 26.3 x 39.7(+11.5)				
MAX WEIGHT IN GRAMS (APPROX.)		31 gms				
STANDARDS		IEC 61810-1				



(Photo For Representation Purpose Only)

### **SALIENT FEATURES**

- High Performance
- Contact Load Capacity up to 40A
- High Reliability
- 6.2 mm Flat Terminals

APPLICATIONS		
Suitable for Automobile	<ul> <li>AMF Diesel Gen Set Control Panels</li> </ul>	<ul> <li>Battery Chargers</li> </ul>
<ul><li>Security Systems</li></ul>	Motors Starters	

#### NOTE:-

- 1) All Specification / Dimensions subject to Tolerance.
- 2) Any Techno commercial changes is / are prerogative of manufacturer / management of the company which can be done without any notice.





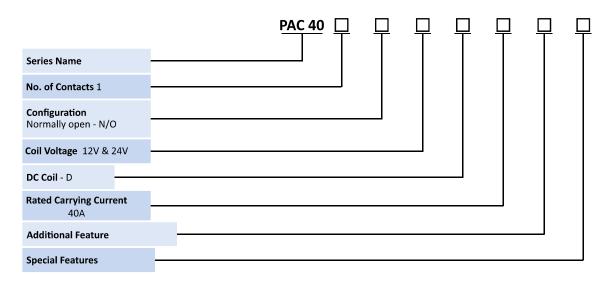




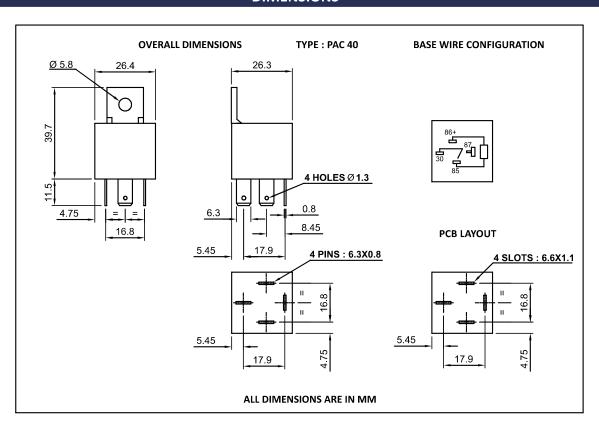
## COIL – DATA (ALL VALUES AT 27°C ± 2°AMBIENT, COLD START)

NOMINAL VOLTAGE (V) (DC)	RESISTANCE IN OHM'S $\pm$ 10% $\Omega$	MUST OPERATE VOLTAGE (V)	MUST RELEASE VOLTAGE (V)	OPERATING POWER FOR COIL DC COIL (W)
12 V	90	9	1.2	1.6
24 V	360	18	2.4	1.6

### **ORDERING CODE FOR RELAY**



### **DIMENSIONS**



NOTE:-1) In case no tolerance shown in outline dimensions: Outline dimension 1mm, tolerance should be ±0.2mm

Outline dimension 1mm and 5mm, tolerance should be ±0.3mm Outline dimension 5mm tolerance should be±0.4mm

2) The tolerance without indicating for PCB layout is always ±0.2mm







